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encrypt

AND

AND

decrypted

AND

AND

circuit

AND

AND

**Date of publication of application** — e.g. 19980401 - 19980405

-

AND

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No.	Publication No.	Title
1.	<u>2006 - 135815</u>	ERROR DETECTION ENCODING CIRCUIT, DATA RECORDER HAVING THE ERROR DETECTION ENCODING CIRCUIT AND DATA RECORDING METHOD
2.	<u>2006 - 134141</u>	PROGRAMMABLE DECRYPTION METHOD FOR PRINT DATA USING IC CARD
3.	<u>2006 - 086594</u>	IMAGE PROCESSING APPARATUS, IMAGE PROCESSING METHOD, STORAGE MEDIUM STORING COMPUTER-READABLE PROGRAM, AND PROGRAM
4.	<u>2006 - 064812</u>	ENCRYPTING DEVICE, ENCRYPTING METHOD, AND DECRYPTING DEVICE, DECRYPTING METHOD
5.	<u>2006 - 023957</u>	SEMICONDUCTOR INTEGRATED CIRCUIT AND INFORMATION PROCESSOR
6.	<u>2006 - 023941</u>	DISK ARRAY DEVICE, METHOD FOR CONTROLLING IT, AND PROGRAM
7.	<u>2005 - 354122</u>	COMMUNICATION EQUIPMENT AND ENCRYPTION CIRCUIT
8.	<u>2005 - 303370</u>	SEMICONDUCTOR CHIP, START PROGRAM, SEMICONDUCTOR CHIP PROGRAM, STORAGE MEDIUM, TERMINAL, AND INFORMATION PROCESSING METHOD
9.	<u>2005 - 252705</u>	DATA PROCESSING CIRCUIT AND CONTROL METHOD
10.	<u>2005 - 190090</u>	INFORMATION READER AND INFORMATION READING SYSTEM
11.	<u>2005 - 172866</u>	ENCRYPTION/DECRYPTION SYSTEM
12.	<u>2005 - 110223</u>	INFORMATION TRANSMISSION SYSTEM
13.	<u>2004 - 336178</u>	METHOD FOR PROTECTING DEVICE KEY AND ENCRYPTION APPARATUS AND DECRYPTION APPARATUS USABLE OF ITS METHOD, AND VIDEO TRANSMITTER AND VIDEO RECEIVER
14.	<u>2004 - 335035</u>	APPARATUS, METHOD AND PROGRAM FOR PROCESSING INFORMATION, AND INFORMATION RECORDING MEDIUM
15.	<u>2004 - 304747</u>	SWITCHING POWER SOURCE
16.	<u>2004 - 266495</u>	ENCRYPTION GENERATING DEVICE, ENCRYPTION DECRYPTING DEVICE, ENCRYPTION GENERATING PROGRAM, AND ENCRYPTION DECRYPTING PROGRAM
17.	<u>2004 - 201038</u>	DATA STORAGE DEVICE, INFORMATION PROCESSING APPARATUS MOUNTED THEREWITH, AND DATA PROCESSING METHOD AND PROGRAM THEREOF
18.	<u>2004 - 200762</u>	VIDEO SIGNAL PROCESSING SYSTEM, VIDEO SIGNAL PROCESSING APPARATUS AND METHOD, RECORDING MEDIUM, AND PROGRAM

19. 2004 - 166280 SECURITY INTEGRATED CIRCUIT
20. 2004 - 159232 REMOTE CENTRALIZED CONTROL SYSTEM AND CENTRALIZED CONTROL METHOD FOR NETWORK
21. 2004 - 135270 SOFTWARE USAGE MEASURING DEVICE AND MULTIMEDIA INFORMATION OUTPUT DEVICE
22. 2004 - 023351 METHOD FOR SECURING PROGRAM OF MICROCOMPUTER
23. 2004 - 007472 SEMICONDUCTOR INTEGRATED CIRCUIT, DATA TRANSFER SYSTEM, AND DATA TRANSFER METHOD
24. 2004 - 007260 ENCRYPTION DEVICE, ELECTRONIC APPARATUS, AND ENCRYPTION METHOD
25. 2004 - 007053 SEMICONDUCTOR INTEGRATED CIRCUIT
26. 2003 - 318883 KEY GENERATION DEVICE, ENCRYPTING/DECRYPTING DEVICE AND DATA RECEIVER
27. 2003 - 309544 CIPHER KEY DELIVERY APPARATUS
28. 2003 - 255830 ENCRYPTING AND DECRYPTING DEVICE, AND ENCRYPTING AND DECRYPTING METHOD
29. 2003 - 152701 ADAPTOR FOR DECRYPTING ENCRYPTED INFORMATION
30. 2003 - 101527 KEY MOUNTING SYSTEM AND LSI FOR IMPLEMENTING THE SAME, AS WELL AS KEY MOUNTING METHOD
31. 2003 - 101524 DATA REPRODUCING DEVICE AND DATA RECORDING DEVICE
32. 2002 - 358010 EXPONENTIATION REMAINDER COMPUTING ELEMENT
33. 2002 - 229444 BLOCK ENCRYPTION AND DECRYPTION CIRCUIT
34. 2002 - 123171 ENCRYPTION AND DECRYPTION CIRCUIT
35. 2002 - 077130 PORTABLE REMOTE TERMINAL AND INFORMATION DISTRIBUTION SYSTEM
36. 2001 - 345799 ENCRYPTION COMMUNICATION SYSTEM, ENCRYPTION METHOD, DECRYPTION METHOD, AND ACCESS NETWORK SYSTEM
37. 2001 - 243705 DEVICE AND METHOD FOR RECORDING SOUND DATA AND DEVICE AND METHOD FOR REPRODUCING SOUND DATA
38. 2001 - 202167 COMPUTER AND ITS CONTROL METHOD
39. 2000 - 019959 CIPHER COMMUNICATION SYSTEM, CIPHER COMMUNICATION METHOD AND STORAGE MEDIUM RECORDING CIPHER COMMUNICATION PROGRAM



36 / 39

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2001-345799

(43)Date of publication of application : 14.12.2001

(51)Int.Cl.

H04L 9/12  
G09C 1/00  
H04L 9/20

(21)Application number : 2000-164604

(71)Applicant : NEC CORP

(22)Date of filing : 01.06.2000

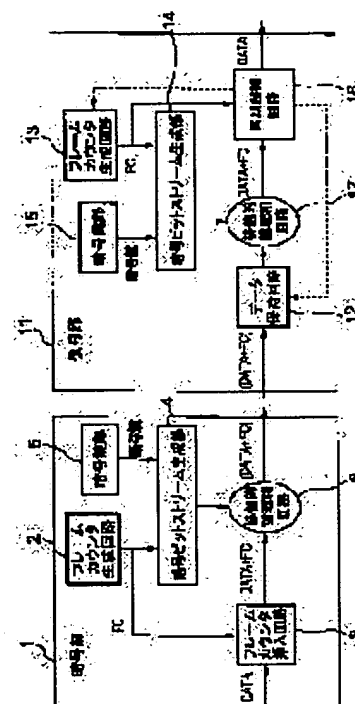
(72)Inventor : FUJIMOTO KOJI

## (54) ENCRYPTION COMMUNICATION SYSTEM, ENCRYPTION METHOD, DECRYPTION METHOD, AND ACCESS NETWORK SYSTEM

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide an encryption communication system in which synchronization can be recovered even when it can not be sustained.

**SOLUTION:** At an encrypting section 1, a frame counter inserting circuit 3 inserts a frame counter into each data frame prior to encryption. An encryption bit stream generating section 4 generates an encryption bit stream based on each frame counter. An exclusive OR circuit 6 encrypts an inserted frame counter by an encryption bit stream corresponding to an inserted data frame. Encrypted data is decrypted by varying the value of the frame counter until a frame counter extracted through decryption matches a frame counter generated an encryption bit stream thereby generating an encryption bit stream.



## LEGAL STATUS

[Date of request for examination]

20.04.2001

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3627623

[Date of registration] 17.12.2004

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2000-019959

(43)Date of publication of application : 21.01.2000

(51)Int.Cl.

G09C 1/00  
H04L 9/08  
H04L 9/14  
H04L 9/30

(21)Application number : 10-182965

(71)Applicant : MATSUSHITA ELECTRIC IND CO LTD

(22)Date of filing : 29.06.1998

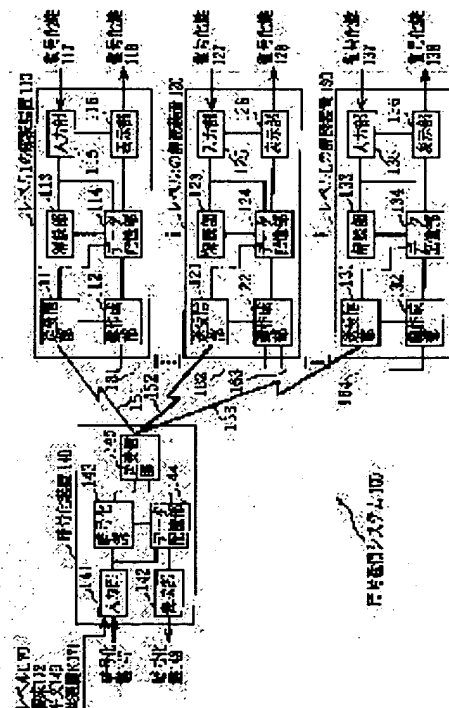
(72)Inventor : WAKAYA AKIYOSHI

## (54) CIPHER COMMUNICATION SYSTEM, CIPHER COMMUNICATION METHOD AND STORAGE MEDIUM RECORDING CIPHER COMMUNICATION PROGRAM

(57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a cipher communication system capable of decrypting encrypted ciphers and reducing the cost for management and distribution of keys by providing the system with a key formation means, an encryption means for forming ciphertext by encryption of plaintext and transmitting the formed ciphertext and a cryptoanalysis means for cryptoanalyzing the transmitted ciphertext.

**SOLUTION:** An encryption section 143 forms the ciphertext by encrypting the plaintext read out by using a prescribed encryption algorithm and outputs an instruction for starting the cipher transmission to a transmission and reception section 145. The key formation section 112 of a cryptoanalysis device 110 of a level 1 outputs a calculated integer b1 as a decryption key to a data storage section 114, outputs an integer a1 as an encryption key and the calculated n1 to a transmission and reception section 111 and transmits the decryption key, etc., through the communication circuit 161 to the key formation section 112 of the cryptoanalysis device 110 of the level 1. A cryptoanalysis section 123 forms the common key by using the decryption key 127 received from an input section 125 and the cipher common key, cryptoanalyzes the ciphertext by using the formed common key and forms the plaintext.



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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **ESPD: Energy-efficient and Secure Pattern-based Data Aggregation for wireless sensor networks**  
Cam, H.; Ozdemir, S.; Nair, P.; Muthuavinashiappan, D.;  
[Sensors, 2003. Proceedings of IEEE](#)  
Volume 2, 22-24 Oct. 2003 Page(s):732 - 736 Vol.2  
Digital Object Identifier 10.1109/ICSENS.2003.1279038  
[AbstractPlus](#) | Full Text: [PDF](#)(384 KB) IEEE CNF  
[Rights and Permissions](#)
- ☐ 2. **SENS: security enhancement to symmetric shared memory multiprocessors**  
Youtao Zhang; Lan Gao; Jun Yang; Xiangyu Zhang; Rajiv Gupta;  
[High-Performance Computer Architecture, 2005. HPCA-11. 11th International Symposium on](#)  
12-16 Feb. 2005 Page(s):352 - 362  
Digital Object Identifier 10.1109/HPCA.2005.31  
[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) IEEE CNF  
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- ☐ 3. **Secure communication based on synchronization of a unified chaotic system**  
Jun-an Lu; Xiaoming Deng; Jin Xie; Liang Chang;  
[Intelligent Control and Automation, 2004. WCICA 2004. Fifth World Congress on](#)  
Volume 2, 15-19 June 2004 Page(s):1264 - 126a Vol.2  
Digital Object Identifier 10.1109/WCICA.2004.1340819  
[AbstractPlus](#) | Full Text: [PDF](#)(546 KB) IEEE CNF  
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- ☐ 4. **A new VLSI implementation of the AES algorithm**  
Liang Deng; Hongyi Chen;  
[Communications, Circuits and Systems and West Sino Expositions, IEEE 2002 International Conference on](#)  
Volume 2, 29 June-1 July 2002 Page(s):1500 - 1504 vol.2  
[AbstractPlus](#) | Full Text: [PDF](#)(351 KB) IEEE CNF  
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- ☐ 5. **A low device occupation IP to implement Rijndael algorithm [cryptography]**  
Panato, A.; Barcelos, M.; Reis, R.;  
[Design, Automation and Test in Europe Conference and Exhibition, 2003](#)  
2003 Page(s):20 - 25 suppl.  
Digital Object Identifier 10.1109/DATE.2003.1186666

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**6. Design space exploration for energy-efficient secure sensor network**

Lin Yuan; Gang Qu;

[Application-Specific Systems, Architectures and Processors, 2002. Proceedings. The IEEE International Conference on](#)

17-19 July 2002 Page(s):88 - 97

Digital Object Identifier 10.1109/ASAP.2002.1030707

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- ➔ Classification codes A: Physics, 7
- ➔ Classification codes A: Physics, 8
- ➔ Classification codes A: Physics, 9
- ➔ Classification codes B: Electrical & Electronics, 0-5
- ➔ Classification codes B: Electrical & Electronics, 6-9
- ➔ Classification codes C: Computer & Control
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circuit simulation and encrypt and decrypt

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NSA will follow-up the design phase with a functional VHDL **simulation** of the designs using ... 7. Time to **encrypt** one block. 8. Time to **decrypt** one block ...  
[jya.com/nsa-aes-r2.htm](http://jya.com/nsa-aes-r2.htm) - 10k - [Cached](#) - [Similar pages](#)

**AES - Rijndael**

Minimization of the **circuit** area using techniques such as resource sharing ... The number of clock cycles necessary to **encrypt** or **decrypt** a block of data is ...  
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**[PDF] High Aberrance AES System Using a Reconstructable Function Core ...**

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 197, completed **simulation** of the software with BCB version 6.0, and validated it. ...  
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 The National Security Agency (NSA) is providing hardware **simulation** support and ... to be performed in **encrypt and decrypt**. For example, the pre-add will ...  
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**[PDF] 4 Testing Methods and Results 5 Summary and Conclusions References:**

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 This paper described the design, implementation, **simulation** and testing of a data compress/**encrypt and decrypt**/decompress ...  
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**Programmable Logic DesignLine | How to implement an open IP ...**

This means that the same key is used to **encrypt and decrypt** the file (Fig 2). ... Leading Internet supplier of prototype **circuit** boards. ...  
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circuit AND simulation AND (encryption and decryption)

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All of the words

☒ 1. WebCADj.PDF [PDF-47K]

Feb 2000

...speed up some algorithms, particularly **circuit simulation**. However,  
these hardware resources...the user can be handled by public-key  
**encryption**, such as PGP. All communications from...cores. For example,  
Cadence provides an **encryption** method that allows encrypted files...  
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Labs Microsoft Research SUN Microsystems S P O N S O R S Centre for  
Development of Advanced Computing, India Cisco Systems Compaq  
Computer Corporation (India) Pvt.

[http://reerca.ac.upc.edu/hipc2000/program.pdf]

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☒ 3. cryptec99++ [PDF-30K]

Dec 2002

...achieved by a boolean **circuit simulation**, without reveal- ing  
her...algorithm exists. The **encryption** matrix is , where is a...random  
permutation matrix. **Encryption**: a plaintext message  
represented...Hamming weight of z). **Decryption**: a cyphertext y is  
decrypted...On the other hand, the **encryption** and **decryption** opera-  
tions are less...

[http://www.eurecom.fr/~nsteam/Papers/cryptec99.pdf]

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☒ 4. REPORT DOCUMENTATION PAGE Form Approved [PDF-334K]

Sep 2001

...Digital Signal Processing (DSP) at high frequencies. These latter  
applications might be suitable for radar, high-speed

**encryption/decryption**, and data compression/decompression. More




importantly some of these device technologies might offer alternative directions...

[<http://inp.cie.rpi.edu/research/mcdonald/frisc/finalre...>]

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May 2001  
1 Abstract Low-Power Domain-Specific Processors for Digital Signal Processing by Arthur Abnous Doctor of Philosophy in Engineering - Electrical Engineering and Computer Sciences University of California, Berkeley  
Professor Jan M.  
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- ☐ **6. [ESD-level circuit simulation impact of interconnect RC-delay on HBM and CDM behavior](#)**  
**Mergens, M.P.J. / Wilkening, W. / Kieseewetter, G. / Mettler, S. / Wolf, H. / Hieber, J. / Fichtner, W., *Journal of Electrostatics*, Jan 2002**  
...2000. ESD-level **circuit simulation** impact of interconnect...model (CDM)-level **circuit simulation** methodology is presented...breakdown. The failure **signature** was validated by...effect is analysed by **circuit simulation** for the first time...  
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- ☐ **7. [FYP Proposals for AY 2001/2002](#)** [PDF-817K]  
May 2001  
...the project is to develop linear RFIC for the transmitter with low DC power consumption. Analysis will be verified with **circuit simulation**. The final circuits is expected to implement on SiGe BICMOS process. Nature Design, analytical, hardware Pre-requisite microelectronics...  
[<http://www.ee.nus.edu.sg/studaffair/ThirdYr/ee/rptFYPf...>]  
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- ☐ **8. [082200Labs.indd](#)** [PDF-15K]  
Feb 2001  
...within the laboratory are used for chip design, device and **circuit simulation**, chip testing, and experimental work on reconfigurable parallel...spacesuit for use in EVA research the Joint Angle and Muscle **Signature** (JAMS) system, a system for studying hand motions and fatigue...  
[<http://www.isr.umd.edu/ISR/about/labs.pdf>]  
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- ☐ **9. [ChengKW BEng\\_eric02.PDF](#)** [PDF-2K]  
Apr 2002  
...Lamp). Soft-switching is also needed. Lamp modelling with **circuit simulation** is helped to understand the control of the whole system. Requirement: Experience in electronics **Signature** Date 28 March 2002 Please return the completed form to Edward...  
[<http://www.ee.polyu.edu.hk/staff/eewclo/ProjectCoord/F...>]  
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- ☐ **10. [hwk6\\_sol.dvi](#)** [PDF-10K]  
Nov 2002  
...T = XT(t) TT s = XT(t) TM The standard **signature** is a different state table realization of the modular MISR **signature**. 15.14 Aliasing analysis (15 points) Z = Y (B C) B Results of **circuit simulation** are as follows: A B C Y Z Good machine...

[<http://www.cse.nd.edu/courses/cse498m/www/homeworks/hw...>]  
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- ☐ **11. [Winter 2001 Ingrid Verbauwhede](#)** [PDF-15K]  
Mar 2001  
...Out VDD Bp Bn Dn,p Sn Sp **Circuit Simulation** Both Time and Data  
treated...Verbauwhede © Prentice Hall 2000 **Signature** Analysis R Counter  
In Counts...Operation mode B0 Normal Scan **Signature** analysis 1 1 0 0 1 0  
Pattern...Memory Self-Test FSM Memory **Signature** Analysis Under Test  
data...  
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...Module Language 141 18 **Signatures** and Structures 143 18.1  
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